

@article{Supernode, author={Chen,Yong and Shang,Weijia }, title={Supernode Transformation With Fixed Processors (Unpublished manuscript - Sanjay will E-mail the PDF)}, }

@INPROCEEDINGS{Exact, author={Chatterjee,Siddhartha and Parker,Erin and Hanlon,Philip J. and Lebeck,Alvin R.}, booktitle={Proceedings of the ACM SIGPLAN 2001 conference on Programming language design and implementation}, title={Exact analysis of the cache behavior of nested loops}, year={2001}, month={May}, pages={286 - 297}, url={<http://dl.acm.org/citation.cfm?id=378859>}, doi={10.1145/381694.378859},}

@INPROCEEDINGS{Reuse-Driven GPU, author={Rawat, Prashant Singh and Hong, Changwan and Ravishankar, Mahesh and Grover, Vinod and Pouchet, Louis-Noel and Rountev, Atlanas and P.Sadayappan}, booktitle={PACT 2016 Accepted Paper - PDF e-mailed by Sanjay}, title={Resource Conscious Reuse-Driven Tiling for GPUs}, year={2016}, month={September}, url={<http://web.cse.ohio-state.edu/presto/pubs/pact16.pdf>}, doi={<http://dx.doi.org/10.1145/2967938.2967967>},}

@INPROCEEDINGS{Libra, author={Jin, Mengyao and Fu, Haohuan and Lv, Zihong and Yang, Guangwen}, booktitle={ Proceedings of the ACM International Conference on Computing Frontiers}, title={Libra: an automated code generation and tuning framework for register-limited stencils on GPUs}, year={2016}, month={May}, pages={92-99}, url={<http://dl.acm.org/citation.cfm?doid=2903150.2903158>}, doi={10.1145/2903150.2903158},}

@INPROCEEDINGS{GPGPU, author={Rawat, Prashant Singh and Hong, Changwan and Ravishankar, Mahesh and Grover, Vinod and Pouchet, Louis-Noel and Rountev, Atlanas and P.Sadayappan}, booktitle={Proceedings of the 9th Annual Workshop on General Purpose Processing using Graphics Processing Unit}, title={Effective resource management for enhancing performance of 2D and 3D stencils on GPUs}, year={2016}, month={March}, pages={92-102}, url={<http://dl.acm.org/citation.cfm?doid=2884045.2884047>}, doi={10.1145/2884045.2884047},}

@inproceedings{Hoeﬂer:2015:SBP:2807591.2807644, author = {Hoeﬂer, Torsten and Belli, Roberto}, title = {Scientific Benchmarking of Parallel Computing Systems: Twelve Ways to Tell the Masses when Reporting Performance Results}, booktitle = {Proceedings of the International Conference for High Performance Computing, Networking, Storage and Analysis}, series = {SC '15}, year = {2015}, isbn = {978-1-4503-3723-6}, location = {Austin, Texas}, pages = {73:1-73:12}, articleno = {73}, numpages = {12}, url = {<http://doi.acm.org/10.1145/2807591.2807644>}, doi = {10.1145/2807591.2807644}, acmid = {2807644}, publisher = {ACM}, address = {New York, NY, USA}, keywords = {benchmarking, data analysis, parallel computing, statistics}, }

@inproceedings{Bender:2016:CA:2935764.2935798, author = {Bender, Michael A. and Demaine, Erik D. and Ebrahimi, Roozbeh and Fineman, Jeremy T. and Johnson, Rob and Lincoln, Andrea and Lynch, Jayson and McCauley, Samuel}, title = {Cache-Adaptive Analysis}, booktitle = {Proceedings of the 28th ACM Symposium on Parallelism in Algorithms and Architectures}, series = {SPAA '16}, year = {2016}, isbn = {978-1-4503-4210-0}, location = {Pacific Grove, California, USA}, pages = {135-144}, numpages = {10}, url = {<http://doi.acm.org/10.1145/2935764.2935798>}, doi = {10.1145/2935764.2935798}, acmid = {2935798}, publisher = {ACM}, address = {New York, NY, USA}, keywords = {cache adaptive, cache oblivious, external memory, memory adaptive, paging algorithms}, }

@inproceedings{Bhaskaracharya:2016:SIA:2837614.2837636, author = {Bhaskaracharya, Somashekaracharya G. and Bondhugula, Uday and Cohen, Albert}, title = {SMO: An Integrated Approach to Intra-array and Inter-array Storage Optimization}, booktitle = {Proceedings of the 43rd Annual ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages}, series = {POPL

'16}, year = {2016}, isbn = {978-1-4503-3549-2}, location = {St. Petersburg, FL, USA}, pages = {526-538}, numpages = {13}, url = {<http://doi.acm.org/10.1145/2837614.2837636>}, doi = {10.1145/2837614.2837636}, acmid = {2837636}, publisher = {ACM}, address = {New York, NY, USA}, keywords = {Compilers, array contraction, memory optimization, polyhedral framework, storage mapping optimization}, }

@INPROCEEDINGS{7429307, author={P. Chatarasi and J. Shirako and V. Sarkar}, booktitle={2015 International Conference on Parallel Architecture and Compilation (PACT)}, title={Polyhedral Optimizations of Explicitly Parallel Programs}, year={2015}, pages={213-226}, doi={10.1109/PACT.2015.44}, ISSN={1089-795X}, month={Oct},}

@INPROCEEDINGS{7429326, author={V. Porpodas and T. M. Jones}, booktitle={2015 International Conference on Parallel Architecture and Compilation (PACT)}, title={Throttling Automatic Vectorization: When Less is More}, year={2015}, pages={432-444}, doi={10.1109/PACT.2015.32}, ISSN={1089-795X}, month={Oct},}

From:
<https://www.cs.colostate.edu/AlphaZ/wiki/> - **AlphaZ**

Permanent link:
<https://www.cs.colostate.edu/AlphaZ/wiki/doku.php?id=melange:papers:fall2016&rev=1472583654>

Last update: **2016/08/30 13:00**

