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journal     = {CoRR},
volume      = {abs/1410.0759},
year        = {2014},
url         = {http://arxiv.org/abs/1410.0759},
timestamp   = {Sun, 02 Nov 2014 11:25:59 +0100},
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keywords = "Active Learning, Compilers, Iterative Compilation, Machine Learning, Sequential Analysis;",  
author = "William Ogilvie and Pavlos Petoumenos and Zheng Wang and Hugh Leather",  
note = "Date of Acceptance: 25/10/2016",  
year = "2016",  
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We describe the design of a convolutional neural network accelerator running on a Stratix V FPGA. The design runs at three times the throughput of previous FPGA CNN accelerator designs. We show that the throughput/watt is significantly higher than for a GPU, and project the performance when ported to an Arria 10 FPGA.

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title       = {Memory-Efficient Backpropagation Through Time},  
journal     = {CoRR},  
volume      = {abs/1606.03401},  
year        = {2016},  
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