



Peer Instruction #4: Logic and State Machines

CS 270, Fall Semester 2014



What will happen to the output of the gate shown below when Input1 is 1 and Input 2 is 1?





What is the column of the truth table for the Output signal, in binary order for Input1 and Input 0 of 00, 01, 10, 11?





Which output signal is asserted for all possible values for for Input1 (most significant) and Input 0 (least significant) in binary order 00, 01, 10, 11?



Logic

A. W, X, Y, Z
B. X, W, Z, Y
C. Y, Z, W, X
D. X, Z, W, Y
E. None of the above

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After RESET, which state will the machine shown below end up in if the inputs are 1, 0, 1, 1, 1, 0, 1





Will the following C program segment print the array elements in order, separated by colons, i.e. "6:7:8"?

int array $[3] = \{6, 7, 8\};$ printf("%d:", array[0]); printf("%d:", *(&array[1])); printf("%d\n", *(array+2)); D. Hard to say!

- A. Yes
- B. No
- C. Will not compile

Arrays and Pointers



Are lines 1 and 2 functionally equivalent to lines 3 and 4 in the program shown below?

int array[3];
 *array++ = 7; *array++ = 8; *array++ = 9;
 int *other = (int *) malloc (3 * sizeof(int));
 other[0] = 7; other[1] = 8; other[2] = 9;



A. Yes B. No C. Almost