

@inproceedings{cohenautomatic, title={Automatic Intra-Array Storage Optimization}, author={Bhaskaracharya, Somashekaracharya G and Bondhugula, Uday and Cohen, Albert }, url={<http://www.csa.iisc.ernet.in/TR/2014/3/paper.pdf>}, series={IISc-CSA-TR-2014-3, Nov 2014 and ACM TOPLAS, Feb 2015}, year={2015}

@inproceedings{kong, title={Compiler/Runtime Framework for Dynamic Dataflow Parallelization of Tiled Programs}, author={Kong, Martin and Pop, Antoniu and Pouchet, Louis-Noël and Govindarajan R and Cohen, Albert and Sadayappan P}, url={<http://dl.acm.org/citation.cfm?id=2687652>}, series={ACM TACO, Jan 2015}, year={2015}

@inproceedings{roshan, title={Compiling Affine Loop Nests for a Dynamic Scheduling Runtime on Shared and Distributed Memory}, author={Dathathri, Roshan and Mullapudi, Ravi Teja and Bondhugula, Uday}, url={<http://drona.csa.iisc.ernet.in/%7euday/publications/dynamic-scheduling-topc-draft.pdf>}, series={TOPC 2016}, year={2016}

@inproceedings{Peter, title={Profitable Scheduling on Multiple Speed-Scalable Processors}, author={Kling, Peter and Pietrzyk, Peter}, url={<http://dl.acm.org/citation.cfm?id=2809872>}, series={ACM TOPC, Oct 2015}, year={2015}

@article{Ravishankar:2014, author = {Ravishankar, Mahesh and Eisenlohr, John and Pouchet, Louis-Noël and Ramanujam, J. and Rountev, Atanas and Sadayappan, P.}, title = {Automatic Parallelization of a Class of Irregular Loops for Distributed Memory Systems}, journal = {ACM Trans. Parallel Comput.}, issue_date = {September 2014}, volume = {1}, number = {1}, month = oct, year = {2014}, issn = {2329-4949}, pages = {7:1-7:37}, articleno = {7}, numpages = {37}, url = {<http://doi.acm.org/10.1145/2660251>}, doi = {10.1145/2660251}, acmid = {2660251}, publisher = {ACM}, address = {New York, NY, USA}, }

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