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author    = {Sharan Chetlur and
             Cliff Woolley and
             Philippe Vandermersch and
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title     = {cuDNN: Efficient Primitives for Deep Learning},
journal   = {CoRR},
volume    = {abs/1410.0759},
year      = {2014},
url       = {http://arxiv.org/abs/1410.0759},
timestamp = {Sun, 02 Nov 2014 11:25:59 +0100},
biburl    =
{http://dblp.uni-trier.de/rec/bib/journals/corr/ChetlurWVCTCS14},
bibsource = {dblp computer science bibliography, http://dblp.org}
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 title={Synthesizing benchmarks for predictive modeling},
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 author={Cummins, Chris and Petoumenos, Pavlos and Wang, Zheng and Leather, Hugh},

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 author={Doerfert, Johannes and Grosser, Tobias and Hack, Sebastian},

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title     = "Minimizing the cost of iterative compilation with active learning",
keywords  = "Active Learning, Compilers, Iterative Compilation, Machine Learning, Sequential Analysis;",
author    = "William Ogilvie and Pavlos Petoumenos and Zheng Wang and Hugh Leather",
note      = "Date of Acceptance: 25/10/2016",
year      = "2016",
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We describe the design of a convolutional neural network accelerator running on a Stratix V FPGA. The design runs at three times the throughput of previous FPGA CNN accelerator designs. We show that the throughput/watt is significantly higher than for a GPU, and project the performance when ported to an Arria 10 FPGA.

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             R{\'{e}}mi Munos and
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title     = {Memory-Efficient Backpropagation Through Time},
journal   = {CoRR},
volume    = {abs/1606.03401},
year      = {2016},
url       = {http://arxiv.org/abs/1606.03401},
timestamp = {Fri, 01 Jul 2016 17:39:49 +0200},
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