

@inproceedings{Bandishti:2012:TSC:2388996.2389051, author = {Bandishti, Vinayaka and Pananilath, Irshad and Bondhugula, Uday}, title = {Tiling Stencil Computations to Maximize Parallelism}, booktitle = {Proceedings of the International Conference on High Performance Computing, Networking, Storage and Analysis}, series = {SC '12}, year = {2012}, isbn = {978-1-4673-0804-5}, location = {Salt Lake City, Utah}, pages = {40:1-40:11}, articleno = {40}, numpages = {11}, url = {<http://dl.acm.org/citation.cfm?id=2388996.2389051>}, acmid = {2389051}, publisher = {IEEE Computer Society Press}, address = {Los Alamitos, CA, USA}, keywords = {compilers, program transformation}, }

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author      = {Sharan Chetlur and
              Cliff Woolley and
              Philippe Vandermersch and
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title       = {cuDNN: Efficient Primitives for Deep Learning},
journal     = {CoRR},
volume     = {abs/1410.0759},
year       = {2014},
url        = {http://arxiv.org/abs/1410.0759},
timestamp  = {Sun, 02 Nov 2014 11:25:59 +0100},
biburl     =
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bibsource = {dblp computer science bibliography, http://dblp.org}
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Sadayappan, P.}, title = {Static and Dynamic Frequency Scaling on Multicore CPUs}, journal = {ACM  
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{26}, url = {http://doi.acm.org/10.1145/3011017}, doi = {10.1145/3011017}, acmid = {3011017},  
publisher = {ACM}, address = {New York, NY, USA}, keywords = {Affine Programs, CPU Energy,  
Static Analysis, Voltage and Frequency Scaling}, }
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@inproceedings{Pouchet:2013:PDR:2435264.2435273, author = {Pouchet, Louis-Noel and Zhang,  
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location = {Monterey, California, USA}, pages = {29-38}, numpages = {10}, url =  
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reuse, high-level synthesis, program transformations}, }
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Kevin and Franchetti, Franz and Pouchet, Louis-Noel and Sadayappan, P.}, title = {When  
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{10.1145/2499370.2462187}, acmid = {2462187}, publisher = {ACM}, address = {New York, NY,  
USA}, keywords = {affine scheduling, autotuning, compiler optimization, loop transformations,  
program synthesis}, }
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title={Synthesizing benchmarks for predictive modeling},
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author={Cummins, Chris and Petoumenos, Pavlos and Wang, Zheng and Leather,  
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author={Doerfert, Johannes and Grosser, Tobias and Hack, Sebastian},
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title      = "Minimizing the cost of iterative compilation with active
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keywords   = "Active Learning, Compilers, Iterative Compilation, Machine
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author     = "William Ogilvie and Pavlos Petoumenos and Zheng Wang and Hugh
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note      = "Date of Acceptance: 25/10/2016",
year      = "2016",
month     = "10",
booktitle = "The International Symposium on Code Generation and Optimization
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Adrian M. and Chung, Eric S. and Chiou, Derek and Constantinides, Kypros and Demme, John and
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Michael and Hauck, Scott and Heil, Stephen and Hormati, Amir and Kim, Joo-Young and Lanka,
Sitaram and Larus, James and Peterson, Eric and Pope, Simon and Smith, Aaron and Thong, Jason and
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[download]

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@miscellaneous{accelerating-deep-convolutional-neural-networks-using-specialized-hardware, author
= {Kalin Ovtcharov, Olatunji Ruwase, Joo-Young Kim, Jeremy Fowers, Karin Strauss, Eric Chung}, title
= {Accelerating Deep Convolutional Neural Networks Using Specialized Hardware}, booktitle = {},
year = {2015}, month = {February}, abstract = {
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We describe the design of a convolutional neural network accelerator running on a Stratix V FPGA. The design runs at three times the throughput of previous FPGA CNN accelerator designs. We show that the throughput/watt is significantly higher than for a GPU, and project the performance when ported to an Arria 10 FPGA.

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{https://www.microsoft.com/en-us/research/publication/accelerating-deep-convolutional-neural-netwo
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{}, isbn = {}, }
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@inproceedings{Deitz:2001:ERS:377792.377807, author = {Deitz, Steven J. and Chamberlain, Bradford L. and Snyder, Lawrence}, title = {Eliminating Redundancies in Sum-of-product Array Computations}, booktitle = {Proceedings of the 15th International Conference on Supercomputing}, series = {ICS '01}, year = {2001}, isbn = {1-58113-410-X}, location = {Sorrento, Italy}, pages = {65-77}, numpages = {13}, url = {http://doi.acm.org/10.1145/377792.377807}, doi = {10.1145/377792.377807}, acmid = {377807}, publisher = {ACM}, address = {New York, NY, USA}, }
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@inproceedings{Basu:2015:CTH:2863692.2863932, author = {Basu, Protonu and Hall, Mary and Williams, Samuel and Straalen, Brian Van and Olikier, Leonid and Colella, Phillip}, title = {Compiler-Directed Transformation for Higher-Order Stencils}, booktitle = {Proceedings of the 2015 IEEE International Parallel and Distributed Processing Symposium}, series = {IPDPS '15}, year = {2015}, isbn = {978-1-4799-8649-1}, pages = {313-323}, numpages = {11}, url = {http://dx.doi.org/10.1109/IPDPS.2015.103}, doi = {10.1109/IPDPS.2015.103}, acmid = {2863932}, publisher = {IEEE Computer Society}, address = {Washington, DC, USA}, keywords = {Compiler Optimization, Stencil, High-Order, Multigrid, Mehrstellen}, }
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author = {Audrunas Gruslys and R{\e}mi Munos and Ivo Danihelka and Marc Lanctot and Alex Graves}, title = {Memory-Efficient Backpropagation Through Time}, journal = {CoRR}, volume = {abs/1606.03401}, year = {2016}, url = {http://arxiv.org/abs/1606.03401}, timestamp = {Fri, 01 Jul 2016 17:39:49 +0200}, biburl = {http://dblp.uni-trier.de/rec/bib/journals/corr/GruslyMDLG16}, bibsource = {dblp computer science bibliography, http://dblp.org}
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