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Author = {Rajopadhye, S. V. and Purushothaman, S. and Fujimoto, R. M.},
Booktitle = {Proceedings, Sixth Conference on Foundations of Software Technology and Theoretical Computer Science},
Key = {Rajopadhye86b},
Month = {December},
Pages = {488-503},
Publisher = {Springer Verlag, LNCS~241},
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Year = {1986}}
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    Journal = {Journal of {VLSI} Signal Processing},
    Number = 2,
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    program (consisting only of assignments, for loops with affine loop
    limits, and arrays with affine index expressions), can be statically
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title     = {Some Efficient Solutions to the Affine Scheduling Problem.
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journal   = {International Journal of Parallel Programming},
volume    = {21},
number    = {5},
year      = {1992},
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author =      {Collard, J-F. and Barthou, D. and Feautrier, P.},  
title =       {Fuzzy Array Data Flow Analysis},  
journal =     {Journal of Parallel and Distributed Computing},  
year =        1997,  
volume =      40,  
number =      2,  
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booktitle =   {Proceedings of the International Conference on Compiler  
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year =        2010,  
series =      {LNCS},  
address =     {Paphos, Cyprus},  
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               Philippe Vandermersch and
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journal     = {CoRR},
volume      = {abs/1410.0759},
year        = {2014},
url         = {http://arxiv.org/abs/1410.0759},
timestamp   = {Sun, 02 Nov 2014 11:25:59 +0100},
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{http://dblp.uni-trier.de/rec/bib/journals/corr/ChetlurWVCTCS14},
bibsource   = {dblp computer science bibliography, http://dblp.org}
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title={Synthesizing benchmarks for predictive modeling},

author={Cummins, Chris and Petoumenos, Pavlos and Wang, Zheng and Leather, Hugh},

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title      = "Minimizing the cost of iterative compilation with active
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keywords   = "Active Learning, Compilers, Iterative Compilation, Machine
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author     = "William Ogilvie and Pavlos Petoumenos and Zheng Wang and Hugh
Leather",
note       = "Date of Acceptance: 25/10/2016",
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Michael and Hauck, Scott and Heil, Stephen and Hormati, Amir and Kim, Joo-Young and Lanka,
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= {Kalin Ovtcharov, Olatunji Ruwase, Joo-Young Kim, Jeremy Fowers, Karin Strauss, Eric Chung}, title
= {Accelerating Deep Convolutional Neural Networks Using Specialized Hardware}, booktitle = {},
year = {2015}, month = {February}, abstract = {

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We describe the design of a convolutional neural network accelerator running on a Stratix V FPGA. The design runs at three times the throughput of previous FPGA CNN accelerator designs. We show that the throughput/watt is significantly higher than for a GPU, and project the performance when ported to an Arria 10 FPGA.

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{https://www.microsoft.com/en-us/research/publication/accelerating-deep-convolutional-neural-networks-using-specialized-hardware/}, address = {}, pages = {}, journal = {}, volume = {}, chapter =
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series = {ICS '01}, year = {2001}, isbn = {1-58113-410-X}, location = {Sorrento, Italy}, pages =
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author      = {Audrunas Gruslys and  
              R{\'}mi Munos and  
              Ivo Danihelka and  
              Marc Lanctot and  
              Alex Graves},  
title       = {Memory-Efficient Backpropagation Through Time},  
journal     = {CoRR},  
volume     = {abs/1606.03401},  
year        = {2016},  
url         = {http://arxiv.org/abs/1606.03401},  
timestamp   = {Fri, 01 Jul 2016 17:39:49 +0200},  
biburl      = {http://dblp.uni-trier.de/rec/bib/journals/corr/GruslysMDLG16},  
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