

Schedule : Fall 2016

This is the tentative schedule of Mélange group for the Fall 2016 semester.

Mélange 1

Purpose: Recent research paper study and discussion from the Reading pool

Meet time & Place : Tuesdays 9:00 AM - 10:00 AM @ CSB 305

| WEEK | DATE | TOPIC | PRESENTER |
|------|------------|---------------|-------------------|
| 1 | 08/23/2016 | Semester Plan | Sanjay Rajopadhye |
| 2 | 08/30/2016 | No Meeting | |
| 3 | 09/06/2016 | TBD | |
| 4 | 09/13/2016 | TBD | |
| 5 | 09/20/2016 | TBD | |
| 6 | 09/27/2016 | TBD | |
| 7 | 10/04/2016 | TBD | |
| 8 | 10/11/2016 | TBD | |
| 9 | 10/18/2016 | TBD | |
| 10 | 10/25/2016 | TBD | |
| 11 | 11/01/2016 | TBD | |
| 12 | 11/08/2016 | TBD | |
| 13 | 11/15/2016 | TBD | |
| 14 | 11/22/2016 | TBD | |
| 15 | 11/29/2016 | TBD | |
| 16 | 12/06/2016 | TBD | |
| 17 | 12/13/2016 | TBD | |
| 18 | 12/20/2016 | TBD | |
| 19 | 12/27/2016 | TBD | |

Reading Pool

Publications

2016

- Prashant Singh Rawat, Changwan Hong, Mahesh Ravishankar, Vinod Grover, Louis-Noel Pouchet, Atlas Rountev, P.Sadayappan, Resource Conscious Reuse-Driven Tiling for GPUs, 2016
- Mengyao Jin, Haohuan Fu, Zihong Lv, Guangwen Yang, Libra: an automated code generation and tuning framework for register-limited stencils on GPUs, 2016

- Somashekaracharya G., Bhaskaracharya, Uday Bondhugula, Albert Cohen, Automatic Storage Optimization for Arrays, 2016
- Prashant Singh Rawat, Changwan Hong, Mahesh Ravishankar, Vinod Grover, Louis-Noel Pouchet, Atlanas Rountev, P.Sadayappan, Effective resource management for enhancing performance of 2D and 3D stencils on GPUs, 2016
- Michael A. Bender, Erik D. Demaine, Roozbeh Ebrahimi, Jeremy T. Fineman, Rob Johnson, Andrea Lincoln, Jayson Lynch, Samuel McCauley, Cache-Adaptive Analysis, 2016
- Somashekaracharya G. Bhaskaracharya, Uday Bondhugula, Albert Cohen, SMO: An Integrated Approach to Intra-array and Inter-array Storage Optimization, 2016
- Hao Zhou, Jingling Xue, Exploiting mixed SIMD parallelism by reducing data reorganization overhead, 2016

2015

- P. Chatarasi, J. Shirako, V. Sarkar, Polyhedral Optimizations of Explicitly Parallel Programs, 2015
- V. Porpodas, T. M. Jones, Throttling Automatic Vectorization: When Less is More, 2015
- Tobias Grosser, Sven Verdoolaege, Albert Cohen, Polyhedral AST Generation Is More Than Scanning Polyhedra, 2015
- Irshad Pananilath, Aravind Acharya, Vinay Vasista, Uday Bondhugula, An Optimizing Code Generator for a Class of Lattice-Boltzmann Computations, 2015
- Protonu Basu, Mary Hall, Samuel Williams, Compiler-Directed Transformation for Higher-Order Stencils, 2015
- Torsten Hoefler, Roberto Belli, Scientific Benchmarking of Parallel Computing Systems: Twelve Ways to Tell the Masses when Reporting Performance Results, 2015

2014

- Sanyam Mehta, Pei-Hung Lin, Pen-Chung Yew, Revisiting loop fusion in the polyhedral framework, 2014
- Kevin Stock, Martin Kong, Tobias Grosser, Louis-Noel Pouchet, Fabrice Rastello, J.Ramanujam, P.Sadayappan, A framework for enhancing data reuse via associative reordering, 2014

2013

- Roshan Dathathri, Ravi Teja Mullapudi, Uday Bondhugula, Compiling Affine Loop Nests for a Dynamic Scheduling Runtime on Shared and Distributed Memory, 2013
- Jithin Jose, Mingzhe Li, Xiaoyi Lu, Krishna Chaitanya Kandalla, Mark Daniel Arnold, Dhableswar K.(DK)Panda, SR-IOV Support for Virtualization on InfiniBand Clusters: Early Experience, 2013

2011

- Louis-Noel Pouchet, Uday Bondhugula, Cedric Bastoul, Albert Cohen, J.Ramanujam, P.Sadayappan, Nicolas Vasilache, Loop transformations: convexity, pruning and optimization, 2011

2001

- Siddhartha Chatterjee, Erin Parker, Philip J. Hanlon, Alvin R. Lebeck, Exact analysis of the cache behavior of nested loops, 2001

From:

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<https://www.cs.colostate.edu/AlphaZ/wiki/doku.php?id=melange:schedule:fall2016&rev=1472530433>

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