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- LUgia
- Lectures: See syllabus
- Staff: See syllabus
- Recitations: See syllabus
- Help session: See syllabus
- Office hours: See syllabus
- Materials on the website and Canvas:
- http://www.cs.colostate.edu/~cs270

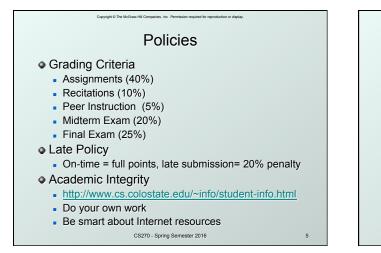
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Assignments and quizzes are posted on website:

- Weekly assignments (mostly) alternate between written and programming assignments.
- Homework assignments are due in hardcopy on original handout on Mon. at 10am.
- Programming assignments are submitted in electronic form Sun. at 10pm.
- Late submission varies depending on the difficulty of the assignment.

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Organization

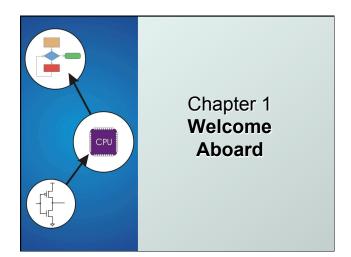
- 1/3 computer hardware: numbers and bits, transistors, gates, digital logic, state machines, von Neumann model, instruction sets, LC-3 architecture
- 1/3 assembly code: instruction formats, branching and control, LC-3 programming, subroutines, memory model (stack)
- 1/3 C programming: data types, language syntax, variables and operators, control structures, functions, pointers and arrays, memory model, recursion, I/O, data structures

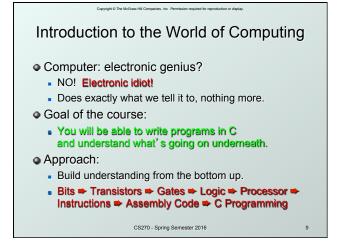
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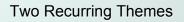
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How to be successful in this class:

- 1) Attend all classes and recitations, info will presented that you can't get anywhere else.
- 2) Do all the homework assignments, ask questions (early!) if you run into trouble.
- 3) Take advantage of lab sessions where help is available from instructors.
- 4) Read the textbook, work through the end of chapter problems.







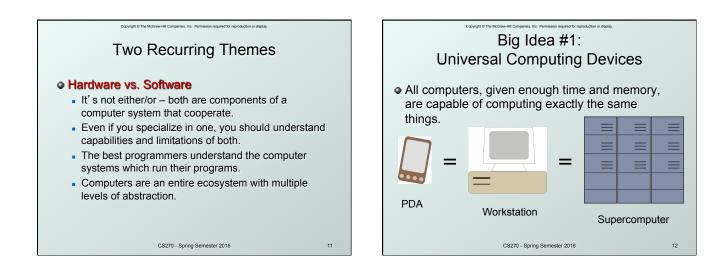
Abstraction

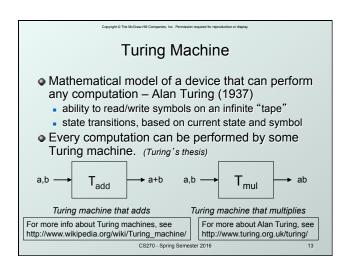
Productivity enhancer – don' t need to worry about details...

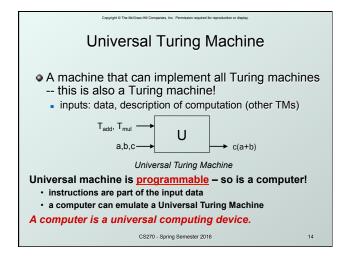
Can drive a car without knowing how

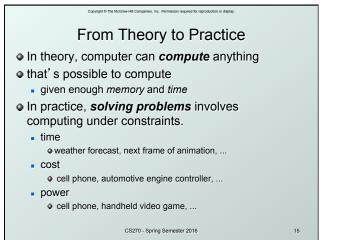
- the internal combustion engine works.
- ...until something goes wrong!
 Where's the dipstick?
 What's a spark plug?
- Important to understand the components and how they work together.

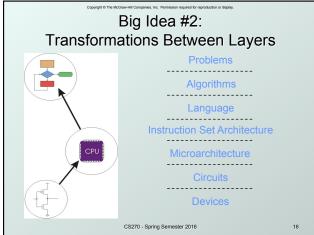
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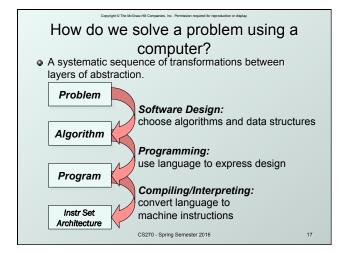


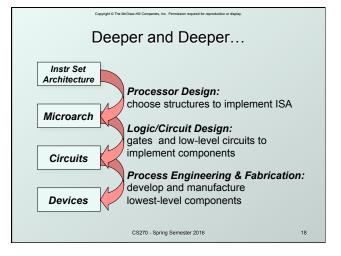












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Descriptions of Each Level	E
Problem Statement	
 stated using "natural language" 	Mic
 may be ambiguous, imprecise 	– (
Algorithm	. (
 step-by-step procedure, guaranteed to finish 	Lo
 definiteness, effective computability, finiteness 	
• Program	
 express the algorithm using a computer language 	
 high-level language, low-level language 	o De
Instruction Set Architecture (ISA)	
 specifies the set of instructions the computer can perform 	
 data types, addressing mode 	
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Capityl © The McGrane HE Compared. Sc. Permanen regard to reputation or depay. Descriptions of Each Level (cont.)

Microarchitecture

- detailed organization of a processor implementation
- different implementations of a single ISA

Logic Circuits

- combine basic operations to realize microarchitecture
- many different ways to implement a single function (e.g., addition)

Devices

properties of materials, manufacturability

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