CS 370: Operating Systems
[CPU SCHEDULING]

# Shrideep Pallickara <br> Computer Science <br> Colorado State University 

1

## Frequently asked questions from the previous class

## survey

$\square$ CPU scheduling
Does the CPU ever make scheduling decisions?
$\square$ Who decides the time interval for each process? The CPU or the scheduler?
$\square$ Are all nonpremeptive systems designed without a timer?
$\square$ Does preemptive scheduling require a preemptive kernel?
$\square$ Is waiting time part of the process' metadata?Do schedulers harvest data every clock cycle to make decisions?

CS370: Operating Systems
Dept. Of Computer Science, Colorado State University

## Topics covered in this lecture

$\square$ Scheduling Algorithms

- SJF
$\square$ Priority Scheduling
$\square$ Round robin schedulingMultilevel feedback queuesLottery scheduling

3


4

## Shortest Job First (SJF) scheduling algorithm

When CPU is available it is assigned to process with smallest CPU burst

Moving a short process before a long process?
Reduction in waiting time for short process
GREATER THAN
Increase in waiting time for long process
Gives us minimum average waiting time for a set of processes that arrived simultaneously
$\square$ Provably Optimal

COLORADO STATE UNIVERSITY

5

## Depiction of SJF in action

| Process | Burst <br> Time |
| :--- | :--- |
| P1 | 6 |
| P2 | 8 |
| P3 | 7 |
| P4 | 3 |



## SJF is optimal ONLY when ALL the jobs are available simultaneously

$\square$ Consider 5 processes A, B, C, D and E
$\square$ Run times are: $\quad 2,4,1,1,1$
$\square$ Arrival times are: $0,0,3,3,3$

SJF will run jobs: A, B, C, D and E
Average wait time: $(0+2+3+4+5) / 5=2.8$
But if you run B, C, D, E and A ?
$\square$ Average wait time: $(7+0+1+2+3) / 5=2.6$ !

## Visualizing the different runs of $A, B, C, D$ and $E$



Average wait time: $(0+2+3+4+5) / 5=2.8$

colorado state university

## Preemptive SJF

What counts as "shortest" is the remaining time left on the task, not its original length
-If you are a nanosecond away from finishing an hour-long task, stay on that task

■ Instead of preempting for a minute long task
Also known, as shortest-remaining-time-first (SRTF)

9

## Preemptive SJF

A new process arrives in the ready queve

- If it is shorter (i.e., shorter time remaining) than the currently executing process?
- Preemptive SJF will preempt the current process


| Process | Arrival | Bur |
| :--- | :--- | :--- |
| P1 | 0 | 8 |
| P2 | 1 | 4 |
| P3 | 2 | 9 |
| P4 | 3 | 5 |

## Characteristics of Preemptive SJF

$\square$ Can suffer from starvation and frequent context switches
-If enough short tasks arrive, long tasks may never complete
Analogy
Supermarket manager switching to SJF to reduce waiting times

## Does Preemptive SJF has any other downsides?

$\square$ Turns out, SJF is pessimal for variance in response timeBy doing the shortest tasks as quickly as possible, SJF necessarily does longer tasks as slowly as possible

Fundamental tradeoff between reducing average response time and reducing the variance in average response time


## SJF IN SCHEDULERS

## Use of SJF in long term schedulers

$\square$
Length of the process time limit

- Used as CPU burst estimate

Motivate users to accurately estimate time limit
Lower value will give faster response times
$\square$ Too low a value?

- Time limit exceeded error
- Requires resubmission!


## The SJF algorithm and short term schedulers

No way to know the length of the next CPU burst
So, try to predict it
Processes scheduled based on predicted CPU bursts

## Prediction of CPU bursts: <br> Make estimates based on past behavior

$\square t_{n}$ : Length of the $n^{\text {th }}$ CPU burst
$\square \tau_{n}$ : Estimate for the $n^{\text {th }}$ CPU burst
$\square \alpha$ : Controls weight of recent and past history$\tau_{n+1}=\alpha t_{n}+(1-\alpha) \tau_{n}$
Burst is predicted as an exponential average of the measured lengths of previous CPU bursts

## $\boldsymbol{\alpha}$ controls the relative weight of recent and past history

$$
\tau_{\mathrm{n}+1}=\alpha \mathrm{t}_{\mathrm{n}}+(1-\alpha) \tau_{\mathrm{n}}
$$Value of $\mathrm{t}_{\mathrm{n}}$ contains our most recent information, while $\tau_{\mathrm{n}}$ stores the past history

$\square \tau_{\mathrm{n}+1}=\alpha \mathrm{t}_{\mathrm{n}}+(1-\alpha) \alpha \mathrm{t}_{\mathrm{n}-1}+\ldots+(1-\alpha)^{j} \alpha \mathrm{t}_{\mathrm{n}-\mathrm{j}}+\ldots+(1-\alpha)^{\mathrm{n}+1} \alpha \tau_{0}$
$\alpha$ is less than $1,(1-\alpha)$ is also less than one
Each successive term has less weight than its predecessor

## The choice of $\alpha$ in our predictive equation

$\square$ If $\alpha=1 / 2$
Recent history and past history are equally weighted
$\square$ With $\alpha=1 / 2$; successive estimates of $\tau$

$$
\mathrm{t}_{0} / 2 \quad \mathrm{t}_{0} / 4+\mathrm{t}_{1} / 2 \quad \mathrm{t}_{0} / 8+\mathrm{t}_{1} / 4+\mathrm{t}_{2} / 2 \quad \mathrm{t}_{0} / 16+\mathrm{t}_{1} / 8+\mathrm{t}_{2} / 4+\mathrm{t}_{3} / 2
$$

$\square$ By the $3^{\text {rd }}$ estimate, weight of what was observed at $t_{0}$ has dropped to $1 / 8$.

## An example: Predicting the length of the next CPU burst



## The choice of $\alpha$ in our predictive equation

$\square \tau_{n+1}=\alpha t_{n}+(1-\alpha) \tau_{n}$
$\square$ If $\alpha \rightarrow 0, \tau_{n+1}=\tau_{n}$
$\square$ Current conditions are transient

If $\alpha=1, \tau_{n+1}=t_{n}$

- Only most recent bursts matter
- History is assumed to be old and irrelevant

Time management is an oxymoron. Time is beyond our control, and the clock keeps ticking regardless of how we lead our lives. Priority management is the answer to maximizing the time we have.

John C. Maxwell

## Priority Scheduling

## Priority Scheduling

Priority associated with each process
$\square$ CPU allocated to process with highest priority
$\square$ Can be preemptive or nonpreemptive

- If preemptive: Preempt CPU from a lower priority process when a higher one is ready


## Depiction of priority scheduling in action

| Process | Burst Time | Priority |
| :--- | :--- | :--- |
| P1 | 10 | 3 |
| P2 | 1 | 1 |
| P3 | 2 | 4 |
| P4 | 1 | 5 |
| P5 | 5 | 2 |

Here: Lower number means
higher priority


## How priorities are set

$\square$ Internally defined priorities based on:
Measured quantities
Time limits, memory requirements, \# of open files, ratio (averages) of I/O to CPU burst

## External priorities

Criteria outside the purview of the OS
$\square$ Importance of process, \$ paid for usage, politics, etc.

## Issue with priority scheduling

Can leave lower priority processes waiting indefinitely
Perhaps apocryphal tale:
MIT's IBM 7094 shutdown (1973) found processes from 1967 !

## Coping with issues in priority scheduling:

Aging
Gradually increase priority of processes that wait for a long time
$\square$ Example:
Process starts with a priority of 127 and decrements every 15 minutes
$\square$ Process priority becomes 0 in no more than 32 hours

## Can SJF be thought of as a priority algorithm?

Priority is inverse of CPU burst
$\square$ The larger the burst, the lower the priority
Note: The number we assign to represent priority levels may vary from system to system


## Round-Robin Scheduling

Similar to FCFS scheduling
$\square$ Preemption to enable switch between processes
Ready queve is implemented as FIFO
$\square$ Process Entry: PCB at tail of queue
$\square$ Process chosen: From head of the queve
CPU scheduler goes around ready queue
Allocates CPU to each process one after the other

- CPU-bound up to a maximum of 1 quantum


## Round Robin: Choosing the quantum

$\square$ Context switch is time consuming
$\square$ Saving and loading registers and memory maps
$\square$ Updating tables
$\square$ Flushing and reloading memory cache
What if quantum is 4 ms and context switch overhead is 1 ms ?
$20 \%$ of CPU time thrown away in administrative overhead

## Round Robin: Improving efficiency by increasing quantum

Let's say quantum is 100 ms and context-switch is 1 ms
Now wasted time is only $1 \%$
But what if 50 concurrent requests come in?
$\square$ Each with widely varying CPU requirements
$\square 1^{\text {st }}$ one starts immediately, $2^{\text {nd }}$ one 100 ms later,..
$\square$ The last one may have to wait for 5 seconds!
A shorter quantum would have given them better service

## If quantum is set longer than mean CPU burst?

## Preemption will not happen very often

Most processes will perform a blocking operation before quantum runs out

Switches happens only when process blocks and cannot continue

## Quantum: Summarizing the possibilities

$\square$ Too short?
Too many context switches
$\square$ Lowers CPU efficiency
Too long?
Poor responses to interactive requests

## A round-robin analogy

$\square$ Hyperkinetic student studying for multiple exams simultaneously If you switch between paragraphs of different textbooks? [Quantum is too short]

- You won't get much done
-If you never switch? [Quantum is too long]
- You never get around to studying for some of the courses

```
Every inch of sky's got a star
Every inch of skin's got a scar
I guess that you've got everything now
Every inch of space in your head
Is filled up with the things that you read
I guess you've got everything now
And every film that you've ever seen
Fills the spaces up in your dreams
That reminds me
...
Every song that l've ever heard
Is playing at the same time, it's absurd
And it reminds me, we've got everything now
                                    Everything Now, Arcade Fire
```


## Multi-level Feedback Queues (MFQ)

```
Most commercial OS including Windows and MacOS, use this scheduling algorithm

\section*{MFQ is designed to achieve several simultaneous goals}

Responsiveness: Run short tasks quickly as in SJF
\(\square\) Low Overhead: Minimize number of preemptions, as in FIFO
Minimize time spent making scheduling decisions

\section*{Starvation-Freedom}

All tasks should make progress, as in Round Robin

\section*{Background tasks}

Defer system maintenance tasks, such as defragmentation, so they do not interfere with user work

Fairness

\section*{Does MFQ achieve all of these?}As with any real system that must balance several, conflicting goals...
\(\square\) MFQ does not perfectly achieve any of these goals
MFQ is intended to be a reasonable compromise in most realworld cases

\section*{MFQ}Extension of round robin
Instead of only a single queue, MFQ has multiple round robin queves

Each queve has a different priority level and time quanta

\section*{Tasks and priorities}
\(\square\) Tasks at a higher priority preempt lower priority tasksTasks at the same priority level are scheduled in round robin fashion

Higher priority tasks have shorter time quanta than lower priority tasks

\section*{MFQ: Example with 4 priority levels}


\section*{Task movements and priority}

Tasks are moved between priority levels to favor short tasks over long ones
Every time a task uses up its time quantum?
It drops a priority level
Every time task yields the processor because it is waiting on I/O? It stays at the same level, or is bumped up a level

If the task completes ... it leaves

\section*{Impact on CPU and I/O bound processes}

A new CPU bound process will start as high priority
\(\square\) But it will quickly exhaust its time quantum and fall to the next lower priority, and then the next ...
An I/O bound process with a modest amount of computing
\(\square\) Will always be scheduled quickly
- Also, keeps the disk busy

Compute bound tasks run with a long time quantum to minimize switching overhead while sharing processor

\section*{What about starvation and fairness?}
\(\square\) If there are too many I/O bound tasks, the compute bound tasks may receive no time on the processor

MFQ scheduler monitors every process to ensure that it is receiving its fair share

For e.g. at each level, maintain two queues
- Tasks whose processes have already reached their fair share are only scheduled if other processes at the same level have also received their fair share
Periodically, processes not receiving their fair share have their tasks increased in priority
\(\square\) Tasks that receive more than their fair share have their priority reduced

\section*{Adjusting priority addresses strategic behavior}

From a selfish point of view, a task can keep its priority high by doing a short I/O request just before its quantum expires

With MFQ this will be detected, and its priority reduced to its fair level


\section*{Lottery scheduling}
\(\square\) Give processes lottery tickets for various system resources
\(\square\) E.g., CPU time
\(\square\) When a scheduling decision has to be made
\(\square\) Lottery ticket is chosen at random
\(\square\) Process holding ticket gets the resource

\section*{Dealing with important processes: All processes are equal, but some processes are more equal than others}

More important processes are given extra tickets
\(\square\) Increase their odds of winning
\(\square\) Let's say there are 100 outstanding tickets
- 1 process holds 20 of these
- Has 20\% chance of winning each lotteryA process holding a fraction \(f\) of tickets
- Will get about a fraction \(f\) of the resource

\section*{Lottery Scheduling: Properties}

\section*{Highly responsive}
\(\square\) Chance of winning is proportional to tickets
\(\square\) Cooperating processes may exchange tickets
Process \(\mathbf{A}\) sends request to \(\mathbf{B}\), and then hands \(\mathbf{B}\) all its tickets for \(\mathbf{a}\) faster response

\section*{Avoids starvation}

Each process holds at least one ticket .... Is guaranteed to have a nonzero probability of being scheduled

\section*{Lottery Scheduling: Properties}

Solves problems that are difficult to handle in other scheduling algorithms
E.g., video server that is managing processes that feed video frames to clients
\(\square\) Clients need frames at 10, 20, and 25 frames \(/ \mathrm{sec}\)
\(\square\) Allocate processes 10,20 and 25 tickets
- CPU divided into approximately 10:20:25

\section*{The contents of this slide-set are based on the following references}
\(\square\) Thomas Anderson and Michael Dahlin. Operating Systems Principles and Practice. \(2^{\text {nd }}\) Edition. ISBN: 978-0985673529. [Chapter 7]
\(\square\) Remzi Arpaci-Dusseau and Andrea Arpaci-Dusseau. Operating Systems: Three Easy Pieces. 1 st edition. CreateSpace Independent Publishing Platform. ISBN-1 3: 9781985086593. [Chapter 9]
\(\square\) Avi Silberschatz, Peter Galvin, Greg Gagne. Operating Systems Concepts, \(9^{\text {th }}\) edition. John Wiley \& Sons, Inc. ISBN-13: 978-1118063330. [Chapter 6]
\(\square\) Andrew S Tanenbaum and Herbert Bos. Modern Operating Systems. \(4^{\text {th }}\) Edition, 2014. Prentice Hall. ISBN: \(013359162 \mathrm{X} / 978-0133591620\). [Chapter 2]```

